

CHONGZHE LI

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EDUCATION

CARNEGIE MELLON UNIVERSITY Pittsburgh, PA

Master of Science in Electrical and Computer Engineering May 2012
Undergraduate Exchange in Electrical and Computer Engineering
Overall GPA: 3.61/4.00

SHANGHAI JIAOTONG UNIVERSITY Shanghai, China

B.E. in Electrical Science and Engineering July 2010
Overall GPA: 3.6/4.00

PROFESSIONAL EXPERIENCE

Carnegie Mellon University, Center for Silicon System Implementation

Research Assistant 09/2010-present

Designed and implemented next generation ultra high frequency transmitters ICs

- Proposed a novel power dividing and combining system for high efficient power amplifier at 45 GHz
- Developed an n-way power combiner design methodology which has state-of-the-art performance in terms of device size and insertion loss.
- Utilized various fabrication techniques to implement off-chip device and interface on-chip and off-chip parts of the system

FC Electronics, Shanghai, China

Assembly line optimization intern 06/2009 – 08/2009

Designed and optimized assembly line process

- Designed and verified testing plans for assembled circuits
- Understood and designed instruction for assembly equipment

Carnegie Mellon University, Electrical and Computer Engineering Department

Teaching Intern

“*RF Circuits and Antennas for Wireless Systems*”, capstone level Fall 2010

- Designed labs, led projects, and enhanced student wireless system and RF theory by designing a digital communication system at 2.4GHz

CORE COURSES

- Advanced Analog IC Design
- Embedded Systems Design
- Microelectromechanical Systems
- RF Circuits and Antennas for Wireless Systems

COMPUTER SKILLS

Languages: C/C++, Java, Assembly, Verilog

Software: Matlab/Simulink, Cadence, ADS, Ansoft HFSS

SELECTED HONORS

- Research Fellowship, Carnegie Mellon University, 2010 - Present
- HKN Electrical and Computer Engineering Honor Society
- National Elite Student Scholarship, Shanghai Jiao Tong University, China, 2007