

Wei Tai

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Education

Ph.D **Carnegie Mellon University**, Dept. Electrical and Computer Engineering
2006 – 2012 (*Expected*)

B.S **National Taiwan University**, Dept. Electrical Engineering
2000 – 2004

Work Experience

- **Teaching Assistant** **Carnegie Mellon University**
2011 18-623, Analog Integrated Circuits
2010 18-721, Advanced Analog Integrated Circuits
- **Research Intern** **Intel**, Radio Integration Research, Hillsboro, OR
2009 – 2010 Power control in OFDM fully integrated power amplifiers
- **Research Intern** **Carley Technology**, Pittsburgh, PA
2008 Magnetically enhanced inductor design
- **2nd lieutenant** **Taiwanese Army**, 584th Armored Brigade
2004 – 2006 Counseling Officer
- **Research Intern** **MediaTek**, Hsinchu, Taiwan
2004 IC back-end physical design

Honor

2011, Analog Devices Outstanding Student Designer Award

Publications

- **W. Tai**, R. L. Carley, D. S. Ricketts, “A Q-Band SiGe Power Amplifier with 17.5 dBm Saturated Output Power and 26% Peak PAE,” In Proceedings of Bipolar/BiCMOS Circuits and Technology Meeting (BCTM), Oct. 2011
- **W. Tai**, H. Xu, A. Ravi, H. Lakdawala, O. B. Degani, L. R. Carley, Y. Palaskas, “A 31.5dBm Outphasing Class-D Power Amplifier in 45nm CMOS with Back-off Efficiency Enhancement by Dynamic Power Control,” In Proceedings of European Solid-State Circuits Conference (ESSCIRC), Sep. 2011